

Part of #2

Sheet 1 of 1

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## U.S. PATENT DOCUMENTS

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## FOREIGN PATENT DOCUMENTS

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						Yes	No

## OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

**Examiner Initial	
Ek	Annala, P., et al., "Electroplated Solder Alloys for Flip Chip Interconnections", <u>Physica Scripta</u> , T69, pp. 115-118, (1997)
Ek	Datta, M., et al., "Electrochemical Fabrication of Mechanically Robust PbSn C4 Interconnections", <u>Journal of the Electrochemical Society</u> , 142 (11), 7 p., (Nov. 1995)
Ek	Honma, S., et al., "Effectiveness of Thin-film Barrier Metals for Eutectic Solder Bumps", <u>Microelectronics International</u> , 14 (3), pp. 47-50, (Sept. 1997)
Ek	Liu, C.Y., et al., "Electron microscopy study of interfacial reaction between eutectic SnPb and Cu/Ni(V)/Al thin film metallization", <u>Journal of Applied Physics</u> , 86 (11), pp. 1-5, (Dec. 1999)

Examiner <i>Ek</i>	Date Considered 10/31/02
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\*Substitute Disclosure Statement Form (PTO-1449)

\*\*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.